Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.028”**

**.028”**

**CATHODE**

**ANODE**

**Top Material: Al**

**Backside Material: Au**

**Bond Pad Size: .004” X .004”**

**Backside Potential: COMMON**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .028” X .028” DATE: 10/21/21**

**MFG: CDI THICKNESS .010” P/N: 1N827**

**DG 10.1.2**

#### Rev B, 7/19/02